Electronic Acknowledgement Receipt FES ID: 1208699 Application Number: 10620045 Confirmation Number: 7529 Method and apparatus for automatic generation of multiple integrated Title of Invention: circuit simulation configuration First Named Inventor: Rajarshi Bhattacharya Ryan, Mason & Lewis, LLP 90 Forest Avenue Correspondence Address: Locust Valley NY 11560 US 5167597517 Filer: Joseph Bernard Ryan Filer Authorized By: Attorney Docket Number: 1-4-2-2-1 Receipt Date: 19-SEP-2006 Filing Date: 15-JUL-2003 Time Stamp: 16:15:38 Application Type: Utility International Application Number: Payment information:

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1		1150-1145_AM1.pdf	334330	yes	8
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	Specification		2	2	
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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.